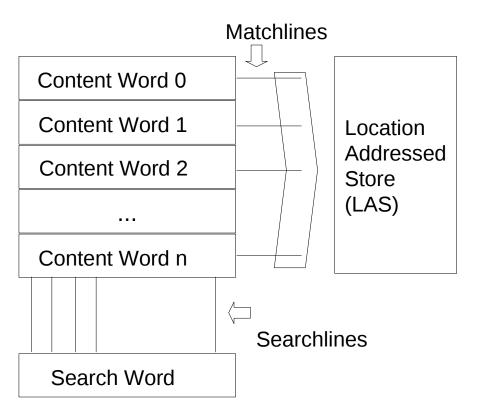
Virtually Cool Ternary Content Addressable Memory

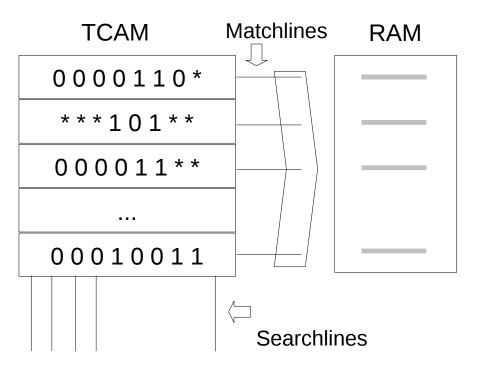
Suparna Bhattacharya, K Gopinath IBM, Indian Institute of Science HotOS XIII, May, 2011

Thanks to Bob Montoye, Vijaylakshmi Srinivasan, Bipin Rajendran, Richard Freitas, John Karidis, C Mohan and Jai Menon



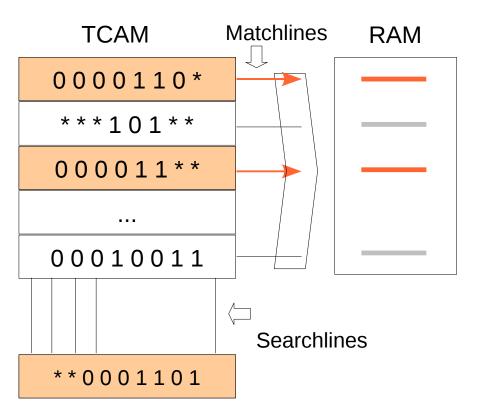
Fast (constant time) key lookup

Parallel match on large data array

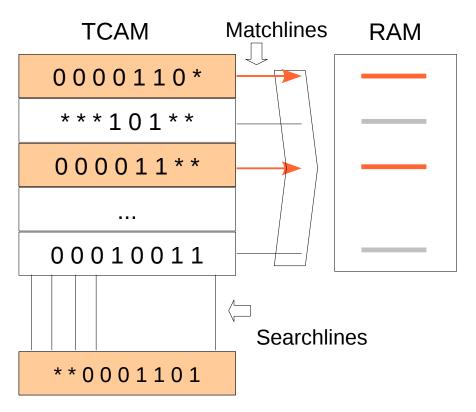


- Fast (constant time) key lookup

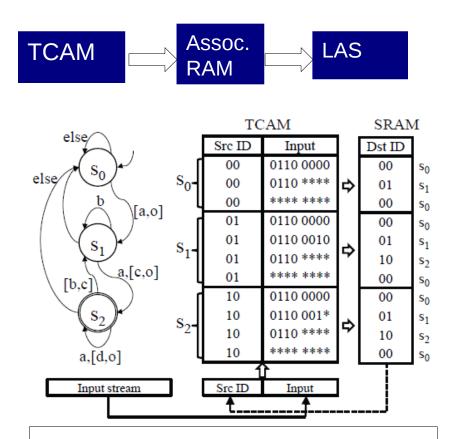
- Parallel match on large data array
- Ternary data: 0, 1, * ("don't care bit")
 - Binary wild-card storage
- Used in High Perf. Network routers



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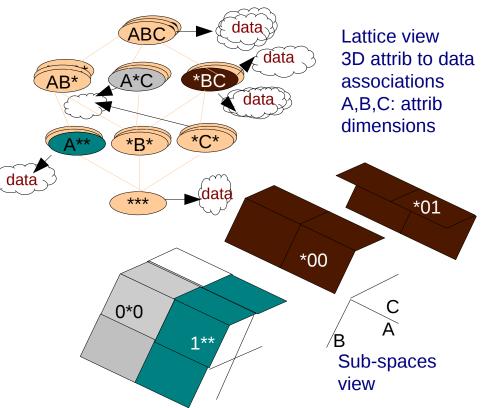


Example: Encoding a DFA in TCAM

Meiners et al, 2010: Fast regular expression matching using small TCAMs for network intrusion detection and prevention

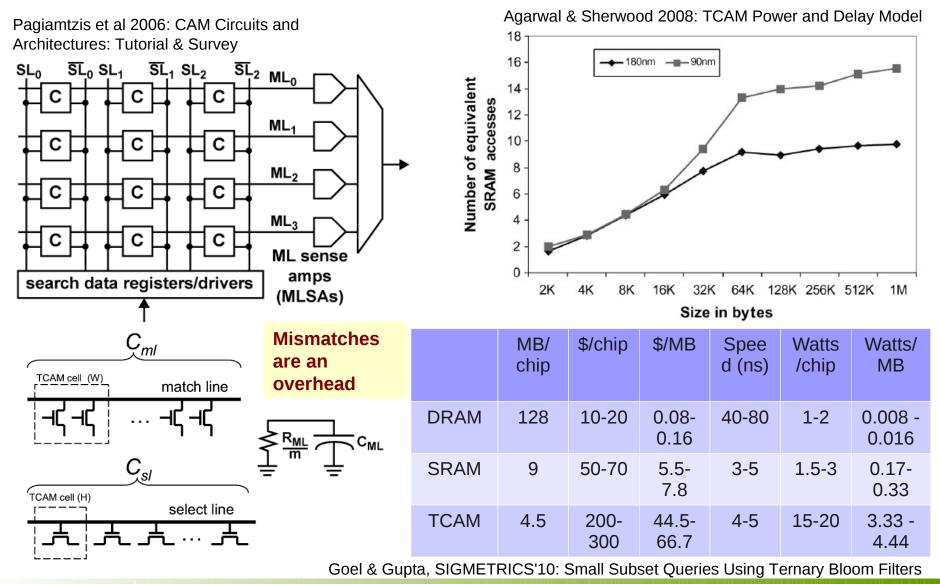
A TCAM is a Natural Candidate for Representation of Space/Time Efficient Associative Search Structures

- Subset query Ternary Bloom Filter
- Similarity search
 - Ternary Locality Sensitive Hashing (TLSH)
 - Approximate nearest neighbor
- Regular expression pattern matching
 - Compact DFA in TCAM
- Database join
 - Multi-match exploitation
- More flexible than radix tree, grid of tries, hash table
 - different constraints (only power of 2 ranges, not ordered, fixed width)



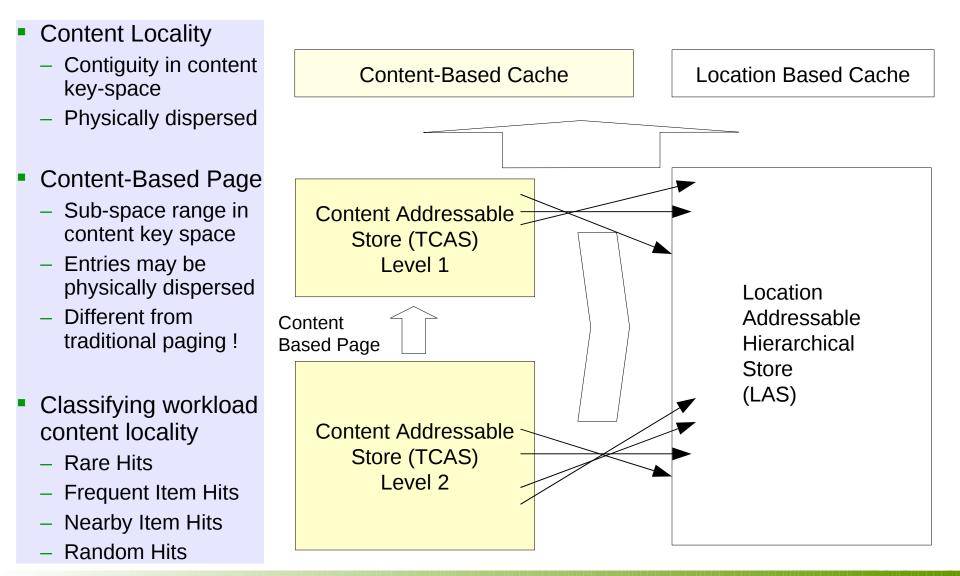
Parallel matching construct on a wild-card storage - powerful abstraction *Ability to simultaneously search through a large number of sub-spaces of a (typically sparse) fixed dimensional space.*

But the Parallel Match Circuit Has a High Power Cost



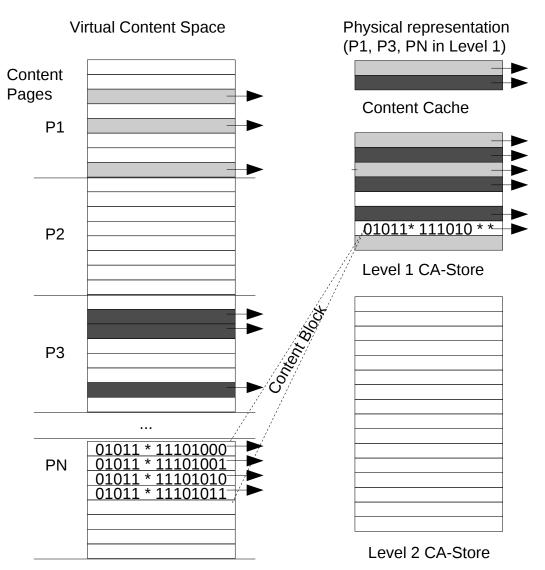


Content Addressable Virtual Memory Hierarchy





Example: Virtual Content Space to TCAS Mapping

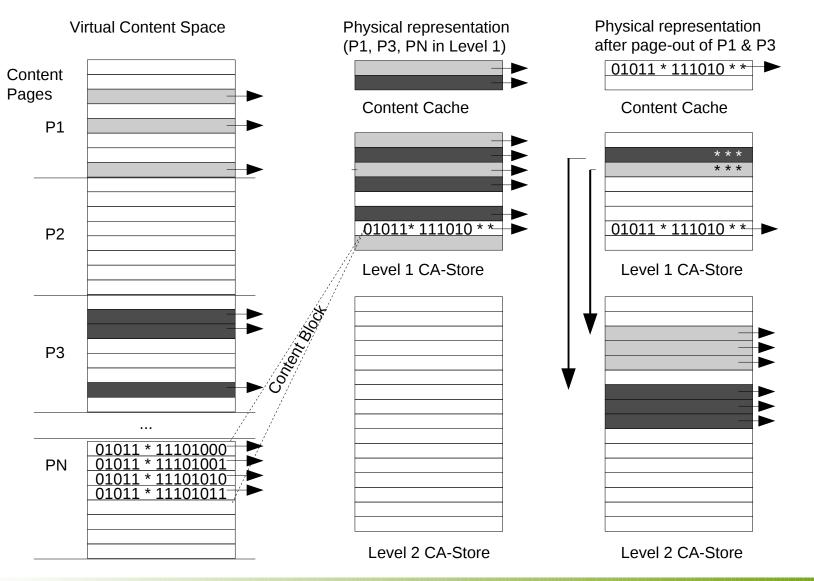


Many interesting questions arise, let us explore one of them in a little more detail...

- How do we save and find TCAM entries that have been paged out to DRAM ?
 - Representing ternary content words in a binary store
 - Easy: with extra bits
 - Indexing ternary content words in a location addressable store
 - What in-memory data structures should we use ?
 - Hash tables ?
 - Integer radix tree ?
 - ??



Example: Virtual Content Space to TCAS Mapping





Implementation Challenges, Design Issues, Debates ...

- Feasibility and Potential: e.g. Power-perf-cost trade-off
 - Understand content locality/working sets of existing workloads
 - TCAM extensions for efficient multi-match ?
 - PCM(Phase Change Memory) based TCAM ?
- TCAS(Ternary Content Addr Store) & LAS(Location Addr Store) management
 - Esp. concurrency, sharing ...
- Choice of interface: How should the abstraction be exposed to applications ?
 - Fully transparent vs Exposed interface ?
- What new possibilities could be opened up if we make content addressability a first class abstraction in virtual memory design ?
 - Too radical or outrageous to be worth it ?
 - Or so crazy that it just might work ?
 - The good news is that it doesn't need to be that radical unless it makes sense
 - $-\,$ e.g. compatibility with location based addressing straightforward

Today is the 150th birth-anniversary of Rabindranath Tagore

Bengali Poet, writer, philosopher, Nobel laureate

"… where words come out from the depth of truth, where tireless striving stretches its arms towards perfection,

where the clear stream of reason has not lost its way into the dreary desert sand of dead habit ..."

"... Pearl fishers dive for pearls, merchants sail in their ships, while children gather pebbles and scatter them again.

They seek not for hidden treasures, they know not how to cast nets."

