Contributors to this Issue

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Marco Vanneschi graduated cum laude in Electronic Engineering from the University of Pisa in 1969. Since 1983 he has been a professor of computer science in the Department of Computer Science of the University of Pisa. He is the national representative in the IFIP TC 10 (Digital System Design) and member of the Working Group 10.3 (Concurrent Systems). His research interests include parallel architectures, concurrent languages run-time

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Bernhard Wagner received the B.S. degree from Pierre et Marie Curie University, Paris VI, France in 1976, and the M.S. degree from Ludwig Maximilians University, Munich, West Germany in 1978, both in mathematics. From 1978 until 1982 he worked in software research at Siemens AG, Munich. Thereafter, he was a research assistant at ETH Zurich, Switzerland, where he received a Ph.D. in computer science in 1986. After a year as a visiting assistant professor at Brigham Young University, Provo UT, he joined Ciba-Geigy AG in Basel where he works at the Scientific Computing Center. His interests include distributed operating systems and computer languages.

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