

Counterexample-Guided Abstraction Refinement for PLCs

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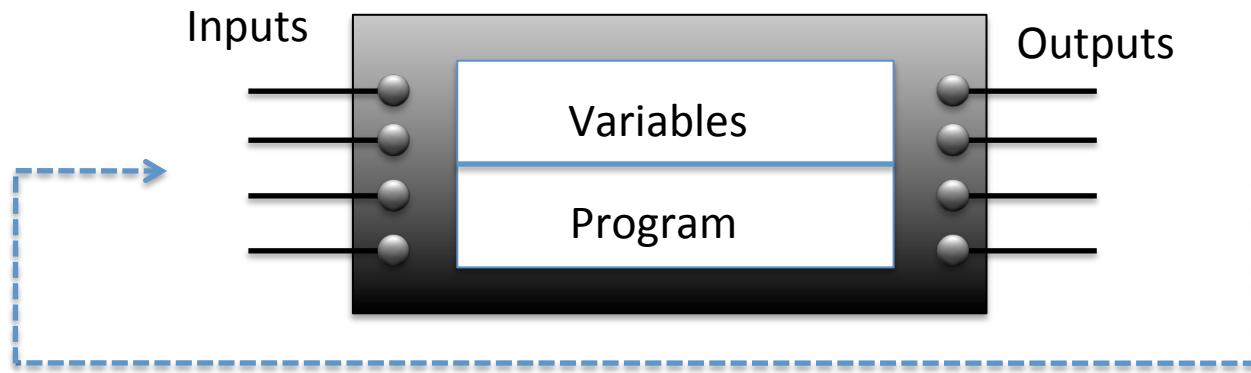
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Overview

- Introduction & Motivation
 - PLCs
 - Model checking PLC programs
- Abstract simulation with refinement
 - Use constraint solving for refinements
 - Different refinement step
 - local variables
 - global variables
- Case studies & implementation
- Conclusion & future work

Programmable Logic Controllers



- Used in the automation industry
- Controlling many safety-critical systems
- Operating in cyclic scanning mode (sensing inputs, processing, writing outputs)
- No non-determinism during cycle
- Different programming languages, here *instruction list*

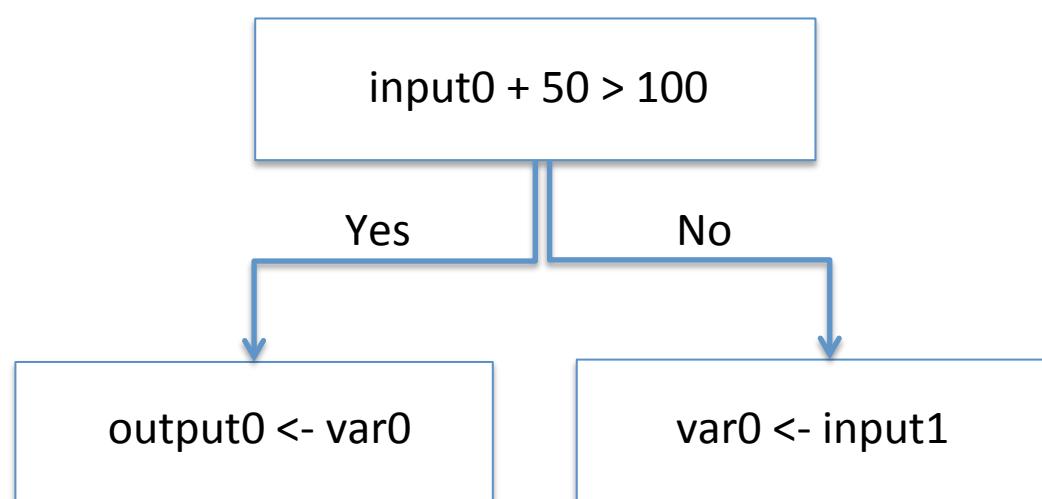
Example IL Program

input0, input1	<i>INPUT</i>
output0	<i>OUTPUT</i>
var0	<i>GLOBAL</i>
Type BYTE	0..255

```
LD  input0
ADD 50
GT  100
JMPC Label

LD  input1
ST   var0
RET

Label:
LD  var0
ST   output0
RET
```



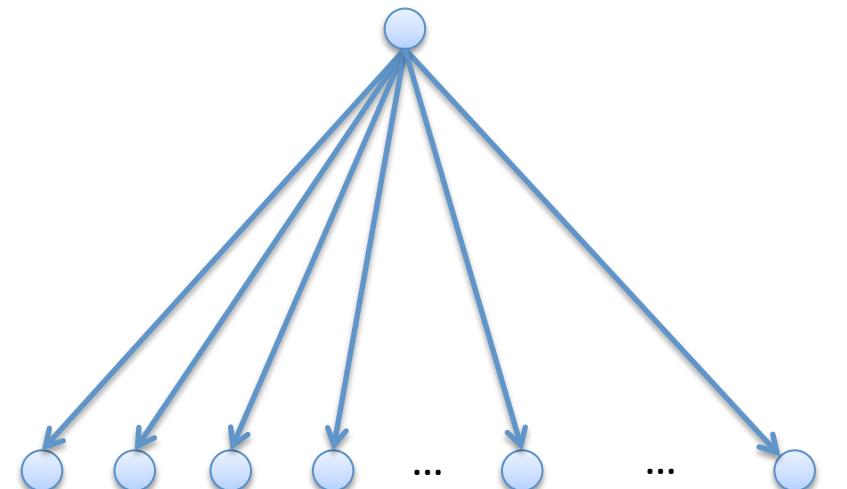
Building the State Space

input0, input1	<i>INPUT</i>
output0	<i>OUTPUT</i>
var0	<i>GLOBAL</i>
Type BYTE	0..255

```
LD  input0
ADD 50
GT 100
JMPC Label

LD  input1
ST  var0
RET

Label:
LD  var0
ST  output0
RET
```



input0 = 0 1 2 3 ... 0 ... 255

input1 = 0 0 0 0 ... 1 ... 255

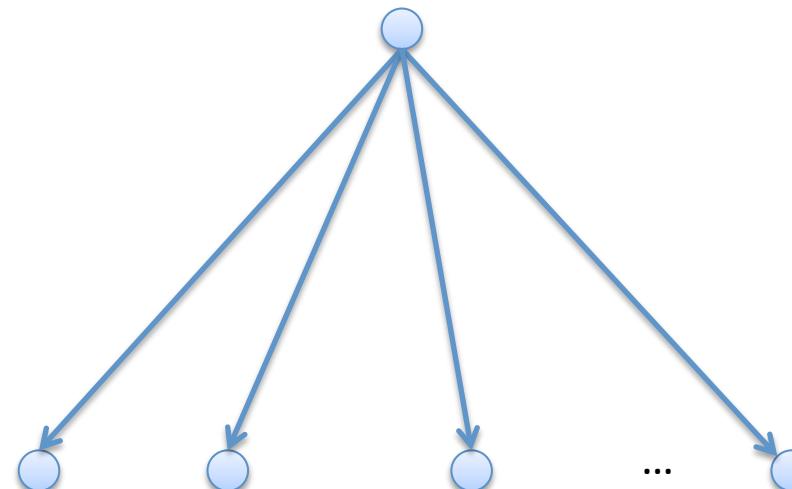
Building the Abstract State Space

input0, input1	<i>INPUT</i>
output0	<i>OUTPUT</i>
var0	<i>GLOBAL</i>
Type BYTE	0..255

```
LD  input0
ADD 50
GT 100
JMPC Label

LD  input1
ST  var0
RET

Label:
LD  var0
ST  output0
RET
```



input0 = [0, 49] [50, 255] [50, 255] ... [50, 255]
input1 = [0, 255] [0, 0] [1, 1] ... [255, 255]

Abstract Domains

- Intervals
 - $[1, 50] + [2, 3] = [3, 53]$
- Bit sets
 - Each bit is 0, 1 or \perp
 - $010\perp\perp 1 \ \& \ 010010 = 0100\perp 0$
- We use the *reduced product* of intervals and bit sets

Example (cont.)

Program	Accumulator
LD input0 ADD 50 GT 100 JMPC Label ...	[0, 255] [50, 305] [0, 1]

- Let's start with $\text{input0} = [0, 255]$
- Condition jump (JMPC) demands a concrete value in accumulator
- This poses a constraint on the abstract value in the accumulator
- Intuitively: Restart cycle with abstract values $[0, 49]$ and $[50, 255]$ for input0 to obey constraint

Constraints on Abstract Values

- $\text{cs}_f(v) : \Leftrightarrow$ Abstract value v is *consistent* under condition f
 - $\text{cs}_{>50}([0, 255])$ is *false*
 - $\text{cs}_{>50}([51, 101]), \text{cs}_{>50}([3, 7])$ are *true*
- $\text{cs}_{\text{sing}}(v) : \Leftrightarrow v$ represents a single value
- Idea:
 - Extend constraints to expressions
 - To guard conditional jumps, etc
 - Next: Formal model for IL programs

SSA Form

Program

```
LD  input0  
ADD 50  
GT  100  
JMPCLabel  
...
```

SSA form

```
acc(0) := input0(0)  
acc(1) := acc(0) + 50  
acc(2) := acc(1) > 100  
guard(cssing(acc(2))  
...
```

- If $cs_{sing}(acc^{(2)})$ is not fulfilled, $input0^{(0)}$ should be split
- Next step: Transform $cs_{sing}(acc^{(2)})$ into a constraint on $input0^{(0)}$

Transforming Constraints

- $\text{cs}_{f_1}(e_1) \vdash \text{cs}_{f_2}(e_2) :\Leftrightarrow \text{cs}_{f_2}(e_2)$ implies the consistency of $\text{cs}_{f_2}(e_2)$
- E.g. $\text{cs}_{>50}(a + 5) \vdash \text{cs}_{>45}(a)$

```
acc0 := input00
acc1 := acc0 + 50
acc2 := acc1 > 100
guard(cssing(acc2))
...
```

$\text{cs}_{sing}(\text{acc}^2)$

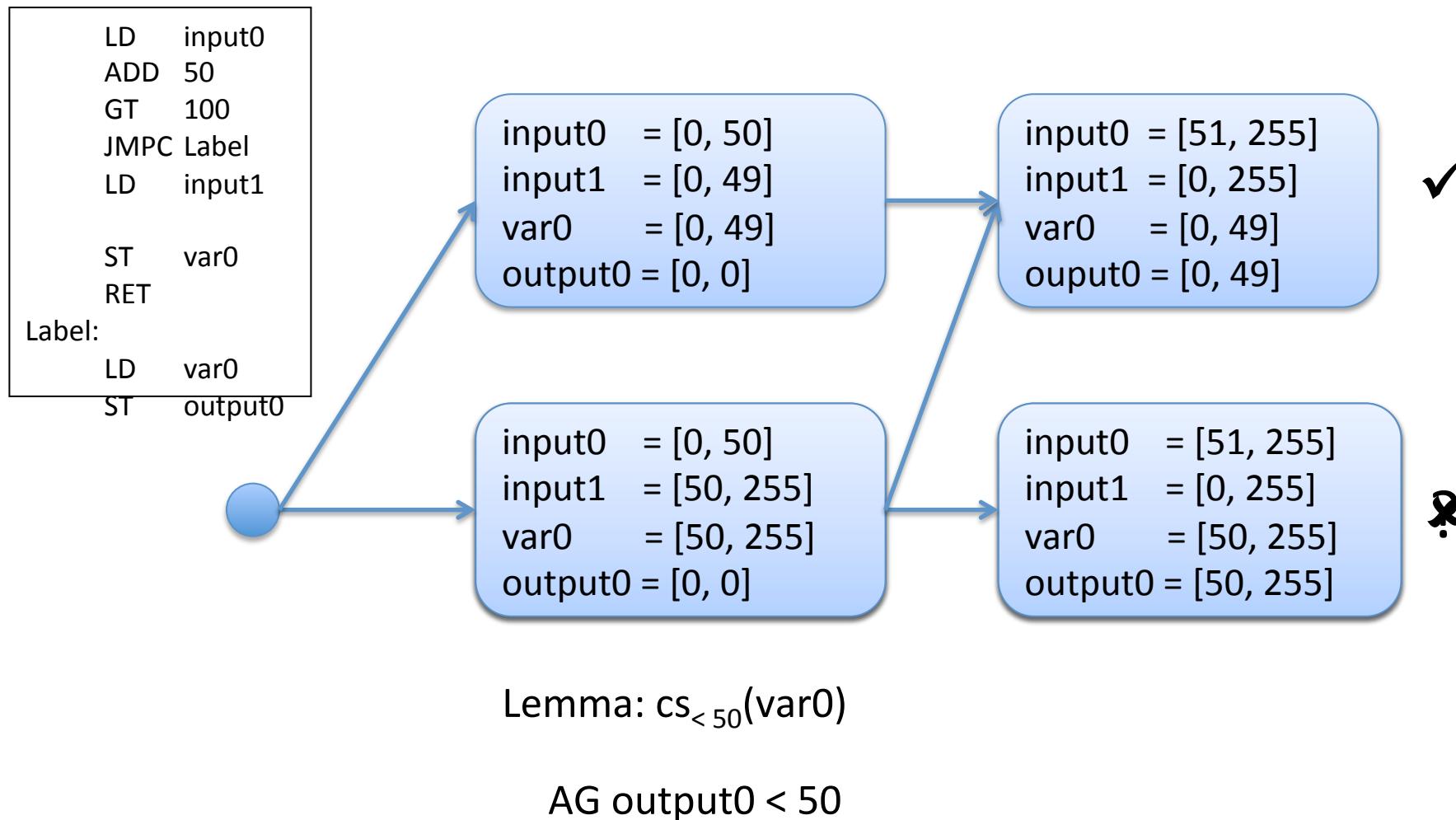
Constraint Guards

- Constraint guards are needed
 - for deterministic control flow
 - for some hardware function blocks (e.g. timers) that require concrete values
 - to guarantee that the atomic propositions of the model-checker have a consistent truth value
- If those constraints are not fulfilled they are transformed into constraints on variables

Refinements of Local Variables

- Refinement loop: Begin with \perp for all inputs
- Transform constraints to constraints on inputs
- Refine inputs and restart cycle
- Each restart refines an abstract value, so the refinement process terminates
- Protect all global variables with single value constraints (no non-determinism in the state space)

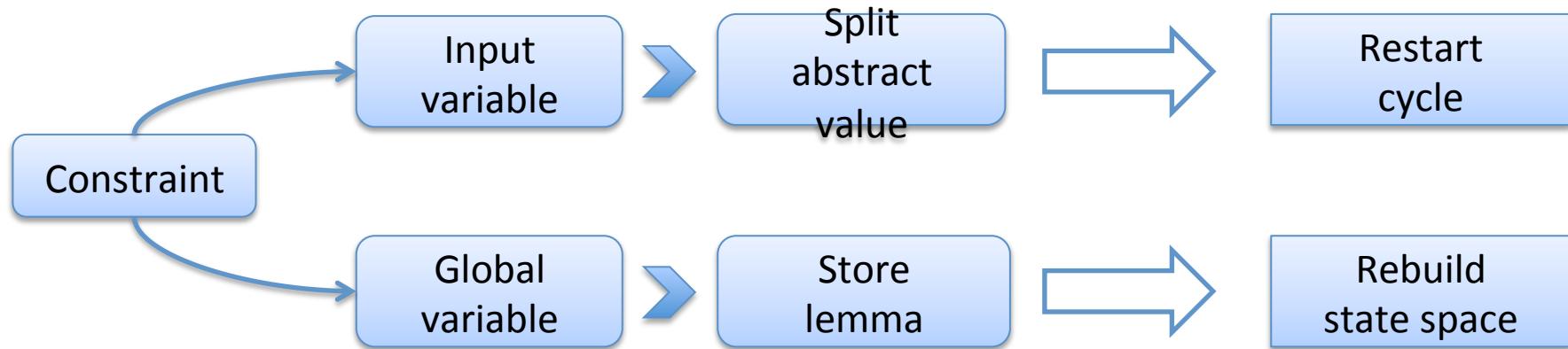
Refinements of Global Variables



Refinements of Global Variables

- Storing abstract values in states possibly allows new behavior
 - A valid ACTL formula is also valid in the concrete state space
 - For an invalid ACTL formula, we have to check whether we found a real counterexample
 - This is achieved by rebuilding the state space using the lemmas as refinements

Overview

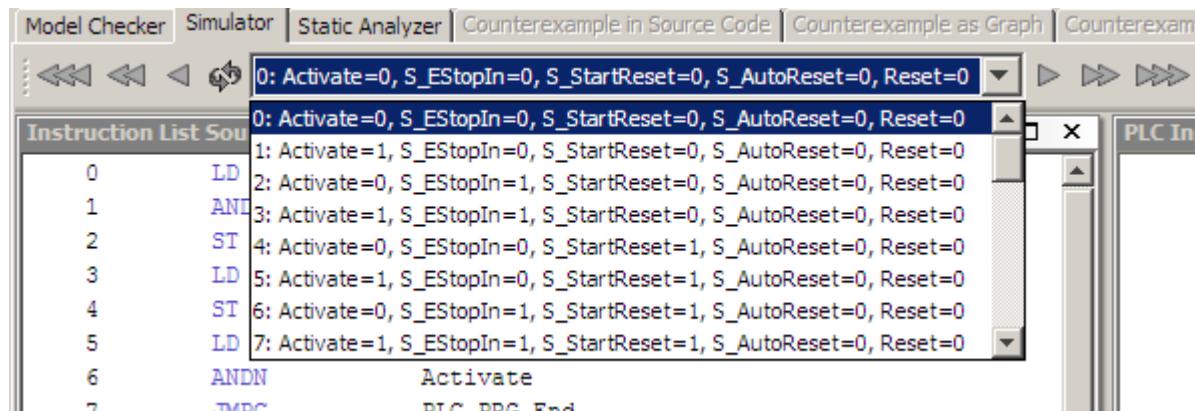


Case Studies

Abstraction technique	# stored states	# created states	State space size [MB]	Time [s]
Without	780 172	199 724 033	1 704	5 633
Only inputs	132 242	3 155 467	351	326
All variables	75 203	1 098 220	163	99

- Function block for monitoring a guard lock (PLCopen)
- 8 Boolean inputs and 5 outputs
- We used an implementation with 300 lines of IL code and 16 internal variables

Implementation in [mc]square



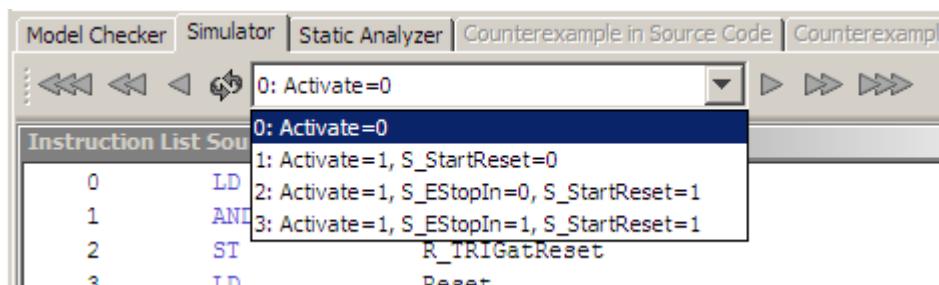
The screenshot shows the [mc]square software interface. The top menu bar includes Model Checker, Simulator, Static Analyzer, Counterexample in Source Code, Counterexample as Graph, Counterexample in Graph, and Counterexample as Text. Below the menu is a toolbar with navigation icons. The main area displays an instruction list source code. The first few lines are:

```
0: Activate=0, S_EStopIn=0, S_StartReset=0, S_AutoReset=0, Reset=0
1: LD
2: AND
3: ST
4: LD
5: ANDN      Activate
6: TMDR      DI C DO E RD
```

The variable definitions at the top of the code are:

```
0: Activate=0, S_EStopIn=0, S_StartReset=0, S_AutoReset=0, Reset=0
1: Activate=1, S_EStopIn=0, S_StartReset=0, S_AutoReset=0, Reset=0
2: Activate=0, S_EStopIn=1, S_StartReset=0, S_AutoReset=0, Reset=0
3: Activate=1, S_EStopIn=1, S_StartReset=0, S_AutoReset=0, Reset=0
4: Activate=0, S_EStopIn=0, S_StartReset=1, S_AutoReset=0, Reset=0
5: Activate=1, S_EStopIn=0, S_StartReset=1, S_AutoReset=0, Reset=0
6: Activate=0, S_EStopIn=1, S_StartReset=1, S_AutoReset=0, Reset=0
7: Activate=1, S_EStopIn=1, S_StartReset=1, S_AutoReset=0, Reset=0
```

VS.



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```
0: Activate=0
1: LD
2: AND
3: ST
4: TMDR      Daset
```

The variable definitions at the top of the code are:

```
0: Activate=0
1: Activate=1, S_StartReset=0
2: Activate=1, S_EStopIn=0, S_StartReset=1
3: Activate=1, S_EStopIn=1, S_StartReset=1
R_TRIIGatReset
```

Conclusion & Future Work

- Conclusion
 - Abstraction refinement for PLC programs
 - Based on constraint resolving
 - Different refinement loop for local and global variables
- Future Work
 - Better constraint resolving using SMT, SAT solvers
 - Incremental rebuild of state space
 - Relational domains constructions